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Please amend the specification as follows:

Please replace the paragraph on page 6 of the specification that begins on line 6 and ends on line 28 with the following paragraph:

FIG. 4 describes operating features of the nonvolatile memory in accordance with the present invention. Block 410 describes data pushed onto the stack from the bottom of stack 24. For an internal block of nonvolatile memory sized to store from 0 to N data words, block 420 indicates that when the parameter value in WORD "N+2" is written, the data in WORD 0 is invalid. Block 430 shows that that the address in the offset register is incremented when stack 24 is written and that the pointer to a valid stack location is maintained. Block 440 indicates that when INTERNAL BLOCK 0 is full, data may then be written to INTERNAL BLOCK 1. Block 450 indicates that when INTERNAL BLOCK 0 is entirely invalid, INTERNAL BLOCK 0 may then be erased. Block 460 indicates that when INTERNAL BLOCK 1 is full, data may then be written to INTERNAL BLOCK 0. Block 470 indicates that when INTERNAL BLOCK 1 is entirely invalid that INTERNAL BLOCK 1 may then be erased.

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